

**Amendments to the Specification**

Please replace the paragraph at page <sup>12</sup>~~13~~<sup>^</sup>, lines 4 through 6 with the following amended paragraph:

*MF 2/23/10*

Optionally, there is a load balancing function ~~[[11]]~~ <sup>16</sup> in front of the host 12 processors, which directs individual transactions to specific host or hosts 12 so as to evenly distribute workload.

Please replace the paragraph at page <sup>16</sup>~~17~~<sup>^</sup>, line 17 with the following amended paragraph:

*MF 2/23/10*

**[[B.]] C. DETAILED DESCRIPTION OF PSDP ARCHITECTURE**

Please add the following new paragraphs at page <sup>16</sup>~~17~~<sup>^</sup>, line <sup>16</sup>~~17~~<sup>^</sup> (before B. DETAILED DESCRIPTION OF PSDP ARCHITECTURE):

*MF 2/23/10*

**B. JPU SOFTWARE COMPONENTS**

Fig. 3 is a diagram of the software components of a JPU 22.

**Communications Layer 300**

- Provides internal communication among nodes
- Includes Job Listener 301 to await requests
- Includes Network Poster 302 to send data when buffer filled, job completed, or at

Host request

**JPU Dispatch/Scheduler 304**

- Receives plan through Communications Layer 300
- Queues Plan
- Schedules/dispatches jobs according to their priority, "fairness" to date, expected resource requirements, and available resources